	Applicati n No.	Applicant(s)
Notic of Allowability	09/763,789	VIRAG ET AL.
	Examiner	Art Unit	
	Son P. Huynh	2623	

-- Th MAILING DATE f this communication appears on th c ver sheet with the c rrespond nce address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to *amendment filed 2/2/2006*.

2. ☒ The allowed claim(s) is/are *1, 3-13 that have been renumbered as 1-12*.

3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

     a)☐ All   b)☐ Some*   c)☐ None   of the:

        1. ☐ Certified copies of the priority documents have been received.

        2. ☐ Certified copies of the priority documents have been received in Application No. _____ .

        3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

     * Certified copies not received: _____ .

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. **THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.

     (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached

        1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____ .

     (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____ .

     Id ntifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) f each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachm nt(s)

1. ☒ Notice of References Cited (PTO-892)

2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)

3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____

4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material

5. ☐ Notice of Informal Patent Application (PTO-152)

6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____ .

7. ☒ Examiner's Amendment/Comment

8. ☐ Examiner's Statement of Reasons for Allowance

9. ☐ Other _____ .

CHRIS KELLEY
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600

DETAILED ACTION

EXAMINER'S AMENDMENT

1.   An examiner's amendment to the record appears below. Should the changes

and/or additions be unacceptable to applicant, an amendment may be filed as provided

by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be

submitted no later than the payment of the issue fee.

2.   Authorization for this examiner's amendment was given in a telephone interview

with Applicant's representative Reitseng Lin (Reg. No. 42,804) on June 19, 2006.

The application has been amended as follows:

In the claims:

1. (currently amended) A method for controlling a video processing apparatus,

the method comprising:

(a) sending a message via a digital bus interconnecting commanding a peripheral

device, connected to and said video processing apparatus, said message causing said

peripheral device to transmit a test signal pattern from an analog output of said

peripheral device;

(b) receiving said test signal pattern from said peripheral device on one of a plurality of analog inputs of said video processing apparatus, wherein said video processing apparatus is capable of distinguishing distinguishes said test signal pattern from other signals received on said analog inputs;

(c) enabling said video processing apparatus to determine which one of said plurality of analog inputs receives said test signal pattern; and

(d) storing data, in said video processing apparatus, associated with said analog input which has received said test signal pattern.

2. (cancelled)

3. (currently amended) The method of Claim 1 2 wherein the step of enabling comprises repetitively selecting each one of said analog inputs of said video processing apparatus to determine which one of said analog inputs receives said test signal pattern.

4. (currently amended) The method of Claim 3 wherein more than one peripheral device is connected to said video processing apparatus and the steps of sending commanding, receiving, enabling and storing are repeated for each one of said peripheral devices.

5. (previously presented) The method of Claim 4 further comprising a step of constructing a map of the analog interconnectivity between each said peripheral device and said video processing device.

6. (previously presented) The method of Claim 3 wherein said test signal pattern is an analog video blanking signal.

7. (original) The method of Claim 1 wherein said video processing apparatus is a digital television.

8. (original) The method of Claim 1 wherein said video processing apparatus is a digital set-top box.

9. (currently amended) The method of Claim 2 1 wherein said digital bus is an IEEE 1394 data bus.

10. (currently amended) A method for defining the interconnectivity of a plurality of peripheral devices to a plurality of analog inputs of a video processing apparatus, said peripheral devices also being interconnected via a digital bus to said video processing apparatus, said video processing apparatus performing the steps of:

(a) selecting one of said plurality of peripheral devices;

(b) sending a command, via said digital bus, to said selected peripheral device for commanding said selected peripheral device to transmit a test signal pattern from an analog output of said selected peripheral device;

(c) receiving said test signal pattern from said selected peripheral device on one of said analog inputs of said video processing apparatus, wherein said video processing apparatus is capable of distinguishing distinguishes said test signal pattern from other signals received on said analog inputs;

(d) monitoring each of said plurality of analog inputs to determine which of said plurality of analog inputs receives said test signal pattern; and

(e) repeating steps (a), (b), (c) and (d) for each of the other ones of said plurality of peripheral devices for automatically constructing a map of the analog interconnectivity of each said peripheral device connected to said video processing apparatus.

11. (original) The method of Claim 10 wherein said digital bus is an IEEE 1394 serial data bus.

12. (currently amended) A method for configuring a video processing apparatus having a plurality of analog inputs and being interconnected via a digital bus to at least first and second peripheral devices, said method comprising:

(a) sending a first command, via said digital bus, to said first peripheral device to switch said first peripheral device into a pass through operating mode;

(b) sending a second command, via said digital bus, to said second peripheral device to transmit a test signal pattern from an analog output of said second peripheral device;

(c) receiving said test signal pattern from said second peripheral device on one of said analog inputs of said video processing apparatus, wherein said video processing apparatus ~~is capable of distinguishing~~ distinguishes said test signal pattern from other signals received on said analog inputs; and

(d) monitoring each of said analog inputs to determine which one of said analog inputs receives said test signal pattern.

13. (original) The method of Claim 12 wherein said digital bus is an IEEE 1394 serial data bus.

Allowable Subject Matter

3.   Claims 1, 3-13 are allowed in view of Applicant's argument file on February 2, 2006.
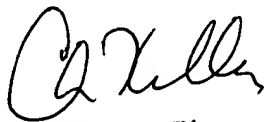
4.   Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

5.   Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Son P. Huynh whose telephone number is 571-272-

7295. The examiner can normally be reached on 9:00 - 6:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Christopher S. Kelley can be reached on 571-272-7331. The fax phone

number for the organization where this application or proceeding is assigned is 571-

273-8300.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a

USPTO Customer Service Representative or access to the automated information

system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Son P. Huynh
June 19, 2006

CHRIS KELLEY
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600